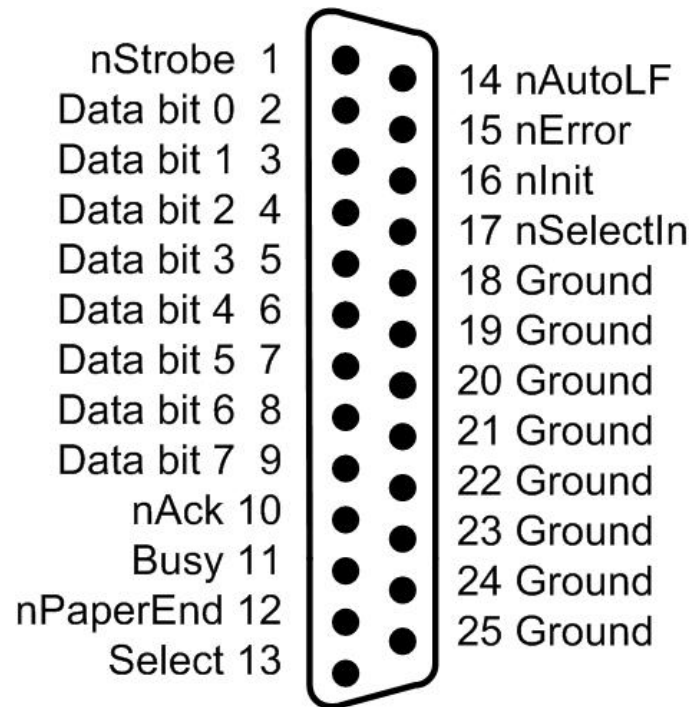


Port data register (base address)		
Bit	Pin no.	Signal name
0	2	Data bit 0
1	3	Data bit 1
2	4	Data bit 2
3	5	Data bit 3
4	6	Data bit 4
5	7	Data bit 5
6	8	Data bit 6
7	9	Data bit 7

Port status register (base address + 1)		
Bit	Pin no.	Signal name
3	15	nError
4	13	Select
5	12	Paper End
6	10	nAck
7	11	Busy

Port control register (base address + 2)		
Bit	Pin no.	Signal name
0	1	nStrobe
1	14	nAutoLF
2	16	nInit
3	17	nSelectIn

## Parallel port registers



Parallel port connector